

APPLICANT(S): EDLIS, Ofir Et al.  
SERIAL NO.: 09/778,818  
FILED: February 8, 2001  
Page 2

### AMENDMENTS TO THE SPECIFICATION

#### In the Specification:

Please replace the paragraph beginning on page 5, line 31 with the following rewritten paragraph:

— Reference is now made to Fig. 3, which illustrates another embodiment of the present invention, wherein the portion of the signal may be recorded between the output of digital processing unit 60 and the input to rake receiver and search engine 70. The portion of the signal may be recorded in a memory device, called memory 65, such as, but not limited to, a read-access memory (RAM) or flash memory. In this embodiment, the power consumption may be improved by turning off rake receiver and search engine 70 while recording the portion of the signal. If searching is accomplished with a lower sampling rate than the sampling rate provided by digital processing unit 60, then the ADC clock frequency may be reduced, thus reducing power consumption. Alternatively, a sampling unit 67 may be implemented between digital processing unit 60 and memory 65, adapted to truncate the sampling rate to the required rate. Using at least one of the alternatives (clock frequency reduction or sampling unit 57) may enable a reduction in the size of memory [[55]] 65.--

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Page 3

#### AMENDMENTS TO THE DRAWINGS

The attached one (1) sheet of drawings includes formal drawings of Fig. 1. This sheet replaces the sheet of drawings including Fig. 1 that was filed on February 8, 2001.

Attachment: One (1) Replacement Sheet